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Application Number

10/047,234

January 14, 2002

(to be used for all correspondence after initial filing)		First Named Inventor	EMANUEL E SHAH		
		Art Unit	2188	2188	
•		Examiner Name	JOHN M ROS	SS	
Total Number of Pages in This Submission 87		Attorney Docket Number			
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**EMANUEL E** 

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#### THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number:

10/047,234

Application filed:

JANUARY 14, 2002

Applicants:

Emanuel E Shah

Title:

High speed low power cacheless computer system

Examiner/GAU:

John M Ross/2188

Austin, June 10, 2004

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

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Technology Center 2100

Sir:

This communication is in response to office action mailed 03/11/2004.

#### **AMENDMENT FOR OFFICE ACTION MAILED 03/11/2004**

In response to the office action mailed 03/11/2004, please amend the above application as indicated for the title, specification and claims each starting on a separate sheet.

## TITLE

### HIGH SPEED LOW POWER CACHELESS COMPUTER SYSTEM

# HIGH PERFORMANCE LOW POWER CACHELESS COMPUTER SYSTEM WITH DETERMINISTIC POWER MANAGEMENT